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## APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*

P&amp;P none

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2002-241827 08/22/2002

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IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 04/21/2004

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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	JAPAN	7	57	7
Examiner's Signature: <i>[Signature]</i> Allowance	Initials: <i>P&amp;P</i>			

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## TITLE

Semiconductor device, method of manufacturing the same, circuit board and electronic apparatus

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